

Date: October 29, 1999  
Atty Docket No. 7217/60017

TO THE ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

Sir:

With reference to the filing in the United States Patent and Trademark  
Office of an application for patent in the name(s) of:

Yuji Yamada  
entitled: AUDIO PROCESSING APPARATUS AND AUDIO REPRODUCING METHOD

Small entity status under 37 CFR 1.9(f) is  
claimed and the amounts shown in parentheses below have been  
employed.

The following are enclosed:

- ☒ Specification
- ☒ 8 Claims(s) (including 2 independent claim(s))
- ☒ Preliminary Amendment
- ☒ Unsigned Oath or Declaration, Power of Attorney & Petition
- ☒ 2 Sheet(s) of Drawings
- ☒ Our check for \$760.00 calculated as follows:

Basic Fee of \$760 (\$380) ..... \$ 760.00

Total Claims in excess of 20 at \$18 (\$9).....\$ .00

Ind. Claims in excess of 3 at \$78 (\$39).....\$ .00

Fee of \$260 (\$130) for Mult. Dep. Claim.....\$ .00

Total Filing Fee ..... \$760.00

Assignment Recording Fee of \$40 ..... \$ .00

- ☒ Certified copy of each of the following to  
substantiate the claim for priority:

<u>Application No.</u>	<u>Filing Date</u>	<u>Country</u>
P10-311308	October 30, 1998	JAPAN

- ☒ Please charge any additional fees required for the filing of this  
application and any other fees required during the pendency of  
this application or credit any overpayment to Deposit Account No.  
03-3125. A duplicate copy of this letter is enclosed.

Respectfully submitted,

COOPER & DUNHAM LLP

By: Jay H. Maioli  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

"Express Mail" Mailing Label No. EL066381790US

Date of Deposit: October 29, 1999

I hereby certify that this Patent Application is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and addressed to the Commissioner of Patents and Trademarks, Box Patent Applications, Washington, D.C. 20231

  
Samuel R. H. 66

Applicant : Yuji Yamada  
7217/60017  
S99P1253US00

Title: AUDIO PROCESSING APPARATUS AND AUDIO  
REPRODUCING METHOD

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yuji Yamada  
Serial No.:  
Filed :  
For : AUDIO PROCESSING APPARATUS  
AND AUDIO REPRODUCING METHOD

October 29, 1999  
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PRELIMINARY AMENDMENT

Hon. Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

Prior to the initial examination of the above-identified application, Applicant respectfully requests that the application be amended as follows.

IN THE SPECIFICATION

Page 20, line 19, after "in" insert -the--.

Page 28, line 1, change "blow" to --below--.

IN THE ABSTRACT OF THE DISCLOSURE

line 1, change "is disclosed which" to --that--;

line 2, delete "means";

line 4, change "filter means" to --filters--;

line 5, change "means";

line 6, delete "with an";

line 7, change "uncorrelation" to --that are not correlated--;

line 8, change "filter means" to --filters--.

IN THE CLAIMS

Please amend claims 1-8 by rewriting same to read as follows.

--1. (Amended) An audio processing apparatus comprising:

[a] first filter means for converting n-channel ( $n \geq 1$ , positive integer) audio signals supplied from at least one signal source into two-channel signals;

a pair of second filter means to which the two-channel output signals from the first filter means are supplied [and which provides] for providing an uncorrelated processing [means] for setting different delay times for respective transfer functions [to] of the [supplied] two-channel input signals; and

an output unit for supplying [output] signals output from the pair of second filter means to left and right loudspeaker units of a headphone.

--2. (Amended) [An] The audio processing apparatus according to claim 1, wherein the pair of second filter means [are constituted by] each comprise a digital filter[, and a pair of] providing uncorrelated processing [means for] by setting delay times for respective

transfer functions [are constituted by] using delay units having different delay times.

--3. (Amended) [An] The audio processing apparatus according to claim 1, wherein the pair of second filter means [are constituted by] each comprise a digital filter[, and a pair of] providing uncorrelated processing [means for] by setting delay times for respective transfer functions [are constituted by] using a delay unit for outputting a plurality of delay times, a multiplier for setting each delay time output to an arbitrary value, and an adder for adding each multiplier output.

--4. (Amended) [An] The audio processing apparatus according to claim 1, wherein the [pair of] first filter means [are constituted by] comprises a pair of digital filters having [characteristics] the same or equivalent [in] transfer characteristics.

--5. (Amended) [An] The audio processing apparatus according to claim 1, further comprising [a] detection means for detecting a direction of movement of the head of a listener wearing the headphone, wherein the transfer functions of the pair of second filter means are made variable depending on an output from the detection means.

--6. (Amended) [An] The audio processing apparatus according to claim 1, wherein the detection means for detecting [a] the direction of movement of the head of [a] the listener wearing the headphone is a piezoelectric vibration gyro, and the transfer functions of the pair of second filter means are made variable depending on an output from the piezoelectric vibration gyro.

--7. (Amended) [An] The audio processing apparatus according to claim 1, wherein the detection means for detecting [a] the direction of movement of the head of [a] the listener wearing the headphone is a geomagnetic azimuth sensor, and the transfer functions of the pair of second filter means are made variable depending on an output from the geomagnetic azimuth sensor.

--8. (Amended) An audio reproducing method comprising:

a first conversion process of converting n-channel ( $n \geq 1$ , positive integer) audio signals supplied from at least one signal source into two-channel signals on the basis of two series of impulse responses from a sound source to left and right ears of a listener;

a second conversion process of independently performing reflective sound adding processes for a pair of uncorrelated processing means for setting a delay time to transfer functions with respect

to two-channel output signals from the first conversion  
[processing means] process, and

a process of reproducing two-channel  
output signals subjected to the second conversion process  
near left and right ears of the listener.--

REMARKS

Claims 1-8 remain in the application and have been  
amended hereby.

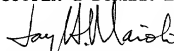
As will be noted from the Declaration, Applicants  
are citizens and residents of Japan and this application  
originated there.

Accordingly, the amendments made to the  
specification are provided to place the application in  
idiomatic English, and the claims are amended to place  
them in better condition for examination.

An early and favorable examination on the merits is  
earnestly solicited.

Respectfully submitted,

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JHM:dmc

4981253

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application for Letters Patent

TITLE: AUDIO PROCESSING APPARATUS AND AUDIO REPRODUCING  
METHOD

INVENTOR(S): YUJI YAMADA

09120906-10000



# AUDIO PROCESSING APPARATUS AND AUDIO REPRODUCING METHOD

## BACKGROUND OF THE INVENTION

### Field of the Invention

The present invention relates to an audio processing apparatus suitably applied to reproduce a stereo audio signal by a headphone device and an audio reproducing method applied to the audio processing apparatus.

### Description of the Related Art

In recent years, as an audio signal (an aural signal) in accompany with a video image of a movie or the like, a multi-channel signal is frequently used which is recorded on the assumption that it is reproduced by loudspeakers placed on both the sides of the video image and the center of the video image and a loudspeaker or the like placed behind an audience or loudspeakers placed on both the sides of the audience. As a result, a sound source in the video image coincides with the position of a sound image which is actually heard, and a sound field having more natural spread is established.

However, when such a sound is to be appreciated by using a conventional headphone device, an acoustic image obtained by an audio input is localized in a head, and the position of the video image does not coincide with the localization position of the sound image. As a result, the sound image is very unnaturally localized. In addition, the focal position of an

audio signal of each channel cannot be separately and independently reproduced. As a matter of course, when only multi-channel sound such as a music or the like is appreciated, unlike reproduction by loudspeakers, a sound is heard from the inside of a head, and the focal positions of the sound image is not separated. A very unnatural sound field is reproduced.

When the sound is heard by a headphone device to improve this phenomenon, in order to obtain a sound field equivalent to that obtained by reproduction by loudspeakers, the following method may be considered. That is, transfer functions from loudspeakers arranged for respective channels in advance to both the ears of a listener are measured or calculated, and these functions are superposed on audio signals by filters such as digital filters or the like. Thereafter, the sound is heard by the headphone device.

FIG. 11 is a block diagram showing a conventional headphone device to which this method is applied. Two left- and right-channel stereo audio signals obtained from input terminals 1L and 1R are converted into digital audio signals by analog/digital converters 2L and 2R, respectively. Two left- and right-channel audio signals output from the analog/digital converters 2L and 2R are supplied to a digital processing circuit 3. The digital processing circuit 3 is constituted by a plurality of digital filters 3LL, 3LR, 3RL, and 3RR and two adders 4L and 4R, and is a circuit which performs a process of

performing conversion such that a reproduced sound field similar to a reproduced sound field obtained when loudspeaker units are actually arranged indoor or the like can be obtained by a headphone device (so-called process of converting stereophonic sound into binaural sound).

As a concrete configuration of the digital processing circuit 3, the following configuration is used. That is, the left-channel audio signal is supplied to the first digital filter 3LL and the second digital filter 3LR, while the right-channel audio signal is supplied to the third digital filter 3RL and the fourth digital filter 3RR. Each of the digital filters has the configuration shown in FIG. 12, for example. The digital filter shown in FIG. 12 is an FIR type filter in which a signal obtained at an input terminal 81 is supplied to delay circuits 82a, 82b, ..., 82m, and 82n which are continuously connected to each other in a plurality of stages. The signal obtained at the input terminal 81 and output signals from the respective delay circuits 82a to 82n are supplied to separate coefficient multipliers 83a, 83b, ..., 83n, and 83o, respectively, in which the signals are multiplied by coefficient values which are independently set, respectively, and the multiplication signals are sequentially added to each other by adders 84a, 84b, ..., 84m, and 84n. An output obtained by adding all the coefficient multiplication signals is obtained at an output terminal 85.

An output from the first digital filter 3LL constituted by the digital filter having the configuration described above and an output from the third digital filter 3RL are supplied to the adder 4L to be added to each other, and a conversion output for the left channel is obtained. An output from the second digital filter 3LR and an output from the fourth digital filter 3RR are supplied to the adder 4R to be added to each other, and a conversion output for the right channel is obtained.

The left-channel output obtained by addition performed in the adder 4L is supplied to a digital/analog converter 5L to be converted into an analog audio signal. The converted analog audio signal is amplified by an amplification circuit 6L for driving a headphone device, and then supplied to a left-ear loudspeaker unit 7L of a headphone device 7. Also, the right-channel output obtained by addition performed in the adder 4R is supplied to a digital/analog converter 5R to be converted into an analog audio signal. The converted analog audio signal is amplified by a amplification circuit 6R by an amplification circuit 6R for driving a headphone device, and then supplied to a right-ear loudspeaker unit 7R of the headphone device 7.

In this case, in the process in the digital processing circuit 3, a principle that an audio signal for stereophonic reproduction is converted into an audio signal for binaural reproduction will be described below with reference to FIG. 13. An left-channel loudspeaker unit SL is arranged in front of a

listener on the left, and a right-channel loudspeaker unit SR is arranged in front of the listener on the right, so that audio signals for stereophonic reproduction can be reproduced from the respective loudspeakers. At this time, assumed that, of sounds reaching the left ear of the listener, a sound reaching the left ear from the left-channel loudspeaker unit SL of the left channel has a transfer function HLL, and a sound reaching the left ear from the right-channel loudspeaker unit SR of the right channel has a transfer function HRL. In addition, assume that, of sounds reaching the right ear of the listener, a sound reaching the right ear from the right-channel loudspeaker unit SR of the right channel has a transfer function HRR, and a sound reaching the right ear from the left-channel loudspeaker unit SL has a transfer function HLR.

The coefficient values of the coefficient multipliers of the respective digital filters are set such that the four transfer functions HLL, HLR, HRL, and HRR are reproduced by arithmetic processes performed in the four digital filters 3LL, 3LR, 3RL, and 3RR, so that two-channel audio signals for stereophonic reproduction are converted into two-channel audio signals for binaural reproduction. In this case, the coefficient values set in the coefficient multipliers of the digital filters respectively are set on the basis of measurement values obtained by measuring the transfer functions of impulse responses from the loudspeaker units of the respective channels

to both the ears in a live room.

According to the processing apparatus proposed as described above, a sound image is localized outside the head of the listener. However, in order to give a sufficient sense of distance to the localized sound image, when the transfer functions from the loudspeakers of the respective channels to both the ears are measured, the transfer functions must be obtained as data having long reverberation times. In order to set the data having long reverberation times in the digital filters, digital filters required by the conventional digital processing circuit 3 having the configuration shown in FIG. 11 have very-large-scale configurations. More specifically, each of the four digital filters required by the digital processing circuit 3 is constituted by approximately 1000 delay circuits connected in series with each other, approximately 1000 coefficient multipliers for multiplying outputs from the respective delay circuits by coefficient values, and approximately 1000 adders for adding multiplication outputs from the respective coefficient multipliers. The digital filters must be caused to perform processes by using transfer functions having reverberation times, and hence the circuit scales of the digital filters are very large. Therefore, quantities of arithmetic processing increase.

The process of converting two-channel audio signals into audio signals for binaural reproduction is described here.

However, when multi-channel audio signals having many channels such as four-channel audio signals for reproducing a sound field which surrounds a listener are converted into audio signals for binaural reproduction, a further large number of digital filters are required, and the circuit configuration disadvantageously has a very large scale.

#### SUMMARY OF THE INVENTION

The present invention has been made in consideration of the above points, and has as its object to provide an audio processing apparatus and an audio reproducing method which can realize a localization of a sound image with a sufficient sense of distance at an arbitrary position for a listener of a headphone device while suppressing a quantity of arithmetic processing of an impulse response.

An audio processing apparatus according to the present invention comprises a first filter means for converting an  $n$ -channel ( $n \geq 1$ , positive integer) audio signal input from at least one sound source into two-channel signals, a pair of second filter means to which a pair of output signals from the first filter means are input and in which transfer functions have uncorrelation, and an output unit for supplying a pair of output signals from the pair of second filter means to left and right loudspeaker units of a headphone.

According to this audio processing apparatus, an arithmetic process of an impulse response is performed by the

first filter means, the process of adding reflective sound components having transfer functions which are not correlated to each other on the left and right to the two-channel signals converted into audio signals for reproduction of a headphone by the arithmetic operation of the impulse response is performed by the second filter means, and a localization of a sound acoustic image can be realized at an arbitrary position with a sufficient sense of distance.

In an audio reproducing method according to the present invention, a first conversion process of converting an  $n$ -channel ( $n \geq 1$ , positive integer) audio signal input from at least one sound source into two-channel signals on the basis of two series of impulse responses from a sound source to left and right ears of a listener and a second conversion process of independently performing reflective sound adding processes by uncorrelated transfer functions for a pair of signals obtained by the first conversion process are performed, and a pair of signals subjected to the second conversion process are reproduced near the left ear and the right ear of the listener.

According to the audio reproducing method, as a sound field formed by audio signals reproduced near the left ear and the right ear of the listener, a sound field in which a sound image is localized at an arbitrary position on the basis of the arithmetic operation of the impulse responses in the first conversion process can be obtained. By the second conversion



process, a localization of a sound image can be realized at an arbitrary position with a sufficient sense of distance.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram showing an entire configuration according to a first embodiment of the present invention;

FIG. 2 is block diagram showing a configuration (Configuration 1) of a first signal processing unit according to the first embodiment of the present invention;

FIG. 3 is a configuration diagram showing an example of a digital filter which can be applied to the first embodiment of the present invention;

FIG. 4 is a block diagram showing a configuration (Configuration 2) of the first signal processing unit according to the first embodiment of the present invention;

FIG. 5 is a configuration diagram showing a configuration of a second signal processing unit according to the first embodiment of the present invention;

FIGS. 6A and 6B are characteristic graphs showing processes in the second signal processing units according to the first embodiment of the present invention;

FIG. 7 is a block diagram showing an entire configuration according to a second embodiment of the present invention;

FIG. 8 is a characteristic graph showing the

relationship between a change in angle of a listener and a change in delay time according to the second embodiment of the present invention;

FIG. 9 is a characteristic graph showing the relationship between a change in angle of a listener and a change in level according to the second embodiment of the present invention;

FIG. 10 is a block diagram showing an entire configuration according to a third embodiment of the present invention;

FIG. 11 is a block diagram showing a configuration of a conventional audio processing apparatus;

FIG. 12 is a configuration diagram showing a digital filter; and

FIG. 13 is an explanatory view for explaining an out-of-head sound image localization process.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

In this embodiment, audio signals for a stereophonic reproduction obtained from input terminals 11L and 11R are converted into audio signals for binaural reproduction, and the audio signals are supplied to a headphone device connected to this apparatus to reproduce the audio signals. FIG. 1 is a block diagram showing an entire configuration of this embodiment. In this configuration, a left-channel signal and a right-channel signal constituting two-channel audio signals for

the stereophonic reproduction are supplied to a left-channel audio signal input terminal 11L and a right-channel audio signal input terminal 11R, respectively. Audio signals obtained at the terminals 11L and 11R are converted into digital audio signals by analog/digital converters 12L and 12R for the respective channels.

The converted audio signals of the respective channels are supplied to a first signal processing unit 13. The first signal processing unit 13 is a circuit for performing the process of converting audio signals into two-channel audio signals for forming a sound field for a headphone reproduction on the basis of two series of impulse responses from a sound source to left and right ears of a listener.

FIG. 2 is a block diagram showing a configuration of the first signal processing unit 13, in which a left-channel audio signal obtained at a left-channel signal input terminal 101L of the first signal processing unit 13 is supplied to a first digital filter 102LL and a second digital filter 102LR, while a right-channel audio signal obtained at a right-channel signal input terminal 101R is supplied to a third digital filter 102RL and a fourth digital filter 102RR.

As each of the digital filters 102LL, 102LR, 102RL, and 102RR, a filter having the same configuration as that of the FIR type digital filter shown in FIG. 12 as a prior art is basically used. Coefficient values multiplied in the coefficient

multipliers of the respective digital filters are set on the basis of actually measured values of the two series of impulse responses from the sound source to the left and right ears of the listener. In case of this embodiment, however, coefficient values each of which has a quantity of arithmetic processing is considerably smaller than that of a conventional coefficient value is used. For example, a digital filter having the following configuration is used. Approximately 250 delay circuits are connected in series with each other, delay outputs from the approximately 250 delay circuits are independently multiplied by coefficients, and the multiplication values are sequentially added. The reason why the quantity of arithmetic processing is decreased will be described later.

An output from the first digital filter 102LL and an output from the third digital filter 102RL are supplied to an adder 103L to be one series of signals. An addition output from the adder 103L is supplied to a left-channel output terminal 104L of the first signal processing unit 13. An output from the second digital filter 102LR and an output from the fourth digital filter 102RR are supplied to an adder 103R to be one series of signals. An addition output from the adder 103R is supplied to a right-channel output terminal 104R of the first signal processing unit 13.

The process of converting audio signals into two-channel audio signals for forming a sound field for the headphone

reproduction in the first signal processing unit 13 is based on the principle explained by using FIG. 13 in the prior art.

As the configuration of digital filters used in the first signal processing unit 13, in place of the configuration using the four digital filters shown in FIG. 2, a configuration using two digital filters shown in FIG. 3 may be used. More specifically, the digital filter shown in FIG. 3 supplies a signal obtained at an input terminal 91 to delay circuits 92a, 92b,..., 92m, and 92n which are continuously connected to each other in a plurality of stages. A signal obtained at the input terminal 91 and output signals from the delay circuits 92a, 92b,..., 92m, and 92n are supplied to separate coefficient multipliers 93a, 93b,..., 93n, and 93o, respectively. The signals are multiplied by coefficient values which are independently set, respectively, and the multiplication signals are sequentially added to each other by adders 94a, 94b,..., 94m, and 94n. An output obtained by adding all the coefficient multiplication signals is obtained at an output terminal 95. The signal obtained from the input terminal 91 and the output signals from the delay circuits 92a to 92n are supplied to coefficient multipliers 96a, 96b,..., 96n, and 96o different from the coefficient multipliers 93a to 93o, respectively. The signals are multiplied by coefficient values which are independently set, respectively, and the multiplication signals are sequentially added to each other by adders 97a, 97b,...,

97m, and 97n. An output obtained by adding all the coefficient multiplication signals is obtained at a second output terminal 98.

Two digital filters each having the configuration described above are prepared. One digital filter is used as the filter 102LL and the filter 102LR of the circuit shown in FIG. 2, and the other digital filter is used as the filter 102RL and the filter 102RR. With this configuration, as to at least the delay circuits constituting the digital filters, the number thereof can be made half the number of delay circuits used when four respective digital filters are used.

The first signal processing unit 13 shown in FIG. 2 may have a circuit configuration shown in FIG. 4 when the positions of left and right sound sources set by audio signals for the stereophonic reproduction (positions where loudspeakers are actually arranged) are laterally symmetrical positions. More specifically, a left-channel audio signal obtained at a left-channel signal input terminal 201L of the first signal processing unit 13 and a right-channel audio signal obtained at a right-channel signal input terminal 201R are supplied to an adder 202L to be added to each other. The addition signal is supplied to a first digital filter 203L. The left-channel audio signal obtained at the left-channel signal input terminal 201L and the right-channel audio signal obtained at the right-channel signal input terminal 201R are supplied to a subtractor 202R to

obtain a value obtained by subtracting the left-channel signal from the right-channel signal. The subtraction signal is supplied to a second digital filter 203R.

As each of the first digital filter 203L and the second digital filter 203R, for example, the FIR type filter shown in FIG. 12 is used. Coefficient values multiplied in the coefficient multipliers of the respective digital filters are set on the basis of actually measured values of two series of impulse responses from the sound sources to the left and right ears of the listener. The number of stages on which the delay circuit, the coefficient multiplier, and the adder are used in each of the digital filters is equal to that of the configuration of each of the digital filters used in the first signal processing unit 13 shown in FIG. 2.

An output from the first digital filter 203L and an output from the second digital filter 203R are supplied to a subtractor 204L to calculate a value obtained by subtracting the output signal from the filter 203R from the output signal from the filter 203L. The subtraction signal is supplied to a left-channel output terminal 205L. The output from the first digital filter 203L and the output from the second digital filter 203R are supplied to an adder 204R to add both the signals, and the addition signal is supplied to a right-channel output terminal 205R.

When the first signal processing unit 13 is constituted

by the configuration shown in FIG. 4, the first signal processing unit 13 can be realized by a simple configuration constituted by two digital filters, two adders, and two subtractors. However, the configuration shown in FIG. 4 can be applied only when the positions of left and right sound sources are laterally symmetrical positions.

Returning to the explanation of FIG. 1, the left-channel audio signal processed by the first signal processing unit 13 is supplied to a second signal processing unit 14L for the left channel, and the right-channel audio signal processed by the first signal processing unit 13 is supplied to a second signal processing unit 14R for the right channel. In the second signal processing units 14L and 14R, reflective sound adding processes are independently performed by transfer functions which are not correlated to each other on the left and right.

As a concrete configuration of the second signal processing units 14L and 14R, for example, the signal processing units 14L and 14R of the respective channels are formed of independent digital filters. In this case, as each of the digital filters, the FIR type digital filter shown in FIG. 12 is used. In the digital filter of each channel, the following operation process is performed. That is, coefficient values of the respective coefficient multipliers are set by a transfer function which is not correlated to the transfer function of the other channel, and reflective sound components (so-called



reverberation sound components) are added on the left and right independently. For example, the frequency characteristics indicated by A in FIG. 6 are set to the left-channel signal, while the frequency characteristics indicated by B in FIG. 6 are set to the right-channel signal, respectively. In case of this embodiment, incidentally, an audio signal is processed as digital data. However, in the characteristic graph in FIG. 6, the frequency characteristics are shown in an analog manner to simplify the explanation.

As the configuration of the second signal processing units 14L and 14R, a configuration using digital filters in which delay amounts can be variably set may be used. FIG. 5 shows a case wherein the second signal processing units 14L and 14R are constituted by digital filters constituting variable delay circuits. A left-channel signal obtained at an input terminal 301L is supplied to a first delay circuit 302L, and a right-channel signal obtained at an input terminal 301R is supplied to a second delay circuit 302R. Each of the delay circuits 302L and 302R is a delay circuit which can delay a signal by a maximum of about 50 ms, and which can derive a plurality of signals having arbitrary delay times set within the maximum delay amount. In this case, the delay circuit 302L has a configuration in which an input signal W1 is derived as signals R1, R2, ..., RN having arbitrary different delay times. The delay circuit 302R has a configuration in which an input

signal W1 is derived as signals R21, R22,..., R2N having arbitrary different delay times. The number of signals derived from each of the delay circuits 302L and 302R is a relatively small number, i.e., about 10, and settings of positions where the signals are derived (i.e., setting of delay amounts of the signals) are independently performed without correlation on the left and right depending on reflective sound components added to the signals of the respective channels at that time.

The signals R1, R2,..., RN extracted from the left-channel delay circuit 302L are multiplied by different coefficient values in different coefficient multipliers 311L, 312L,..., 319L, respectively, and the multiplication signals are supplied to an adder 303L to be added to each other. The addition signal is supplied to a left-channel output terminal 304L. The signals R21, R22,..., R2N extracted from the right-channel delay circuit 302R are multiplied by different coefficient values in different coefficient multipliers 311R, 312R,..., 319R, respectively, and the multiplication signals are supplied to an adder 303R to be added to each other. The addition signal is supplied to a right-channel output terminal 304R. The coefficient values multiplied in the respective coefficient multipliers 311L to 319L and 311R to 319R are fixed values which are predetermined. For example, the level of the signal having a smaller delay amount is increased, and coefficient values are set such that the level gradually

decreases in proportion to an increase in delay amount. In place of the fixed values described above, coefficient values multiplied in the coefficient multipliers may be controlled depending on conditions at that time.

When the second signal processing units 14L and 14R are constituted by the configuration shown in FIG. 5, the setting conditions of reflective sound components can be independently varied on the left and right by setting the delay amounts.

Turning back to the configuration in FIG. 1, the left and right audio signals processed by the second signal processing units 14L and 14R are independently supplied to different digital/analog converters 15L and 15R for the respective channels to be converted into analog audio signals. The left and right two-channel analog audio signals therefrom are amplified by amplifiers 16L and 16R, having relatively small amplification factors, for driving a headphone, and the amplified audio signals are then supplied to headphone connection terminals 17L and 17R, respectively. The audio signals of the respective channels obtained from the headphone connection terminals 17L and 17R are supplied to left and right loudspeaker units 18L and 18R of a headphone device 18 connected to the headphone connection terminals 17L and 17R, respectively, and the audio signals are reproduced from the headphone device 18.

With the configuration described above, a sound field

reproduced by the headphone device 18 and heard by a listener is a preferable sound field which is similar to a sound field formed such that original two-channel audio signals are reproduced by loudspeakers arranged in a room or the like. In this case, as the process in the first signal processing unit 13 according to this embodiment, a process having a relatively small quantity of arithmetic processing is used. For this reason, when signals only processed in the first signal processing unit 13 are supplied to the headphone device, a position where a sound image is localized is a position close to the head of the listener. However, since the process of adding reflective sound components is performed by the second signal processing units 14L and 14R, the sound source can be localized at an arbitrary position with a sufficient sense of distance. In addition, since uncorrelation between the left and right channels is assured in the second signal processing units 14L and 14R, asymmetry of the sound image can be realized, and the forward localization of the sound image is improved.

Therefore, as in case of the processing apparatus shown in FIG. 11 as a prior art, in comparison with a case wherein a conversion process is performed to cause a one-stage digital filter to localize the sound image with a sufficient sense of distance, a circuit configuration can be considerably simplified, and a quantity of arithmetic processing can be reduced. For example, the digital filters constituting the

digital processing circuit 3 shown in FIG. 11 must perform delay processes on about 1,000 stages. However, the digital filters constituting the first signal processing unit 13 in the present configuration may perform delay processes on about 250 stages, and the configuration which is 1/4 the conventional configuration may be sufficient. In the configuration of this embodiment, although the second signal processing units 14L and 14R are required, the second signal processing units 14L and 14R perform only the process of adding reflective sound components. For this reason, as the second signal processing units 14L and 14R, digital filters having circuit scales which are considerably smaller than those of the digital filters constituting the first signal processing unit 13 are sufficient. When the configuration of this embodiment shown in FIG. 1 is used, the circuit configuration which is considerably simpler than the conventional circuit configuration can be employed.

In the explanation up to this, two-channel audio signals are used as audio signals to be input. However, for example, the following process may be performed. That is, one-channel audio signal is input to the audio signal input terminals 11L and 11R, and the position of a sound image localized by the one-channel signal is set at one arbitrary point.

A second embodiment of the present invention will be described below with reference to FIGS. 7 to 9. The same reference numerals as those in FIGS. 1 to 6 explained in the

first embodiment described above denote the same parts in FIGS. 7 to 9, and a description thereof will be omitted.

As in this embodiment, too, audio signals for stereophonic reproduction obtained at input terminals 11L and 11R are converted into audio signals for the binaural reproduction, and the converted audio signals are supplied to a headphone device connected to this apparatus to reproduce the audio signals. In this embodiment, the process called a head tracking process of correcting a phase of a sound field is depending on the direction in which the headphone device faces.

The configuration of this embodiment will be described below. FIG. 7 is a block diagram showing the entire configuration of this embodiment. A left-channel signal and a right-channel signal constituting two-channel audio signals for the stereophonic reproduction are supplied to the left-channel audio signal input terminal 11L and the right-channel audio signal input terminal 11R. Audio signals obtained at the terminals 11L and 11R are converted into digital audio signals by analog/digital converters 12L and 12R for the respective channels, and the digital audio signals are then supplied to the first signal processing unit 13. The first signal processing unit 13 is a circuit for performing the process of converting audio signals into two-channel audio signals for forming a sound field for the headphone reproduction on the basis of two series of impulse responses from sound sources to the left and right

ears of a listener. This circuit is entirely the same as the circuit which has been described in the first embodiment.

The left-channel audio signal processed by the first signal processing unit 13 is supplied to a second signal processing unit 21L for the left channel, and the right-channel audio signal processed by the first signal processing unit 13 is supplied to a second signal processing unit 21R for the right channel. In the second signal processing units 21L and 21R, reflective sound adding processes are independently performed by transfer functions which are not correlated to each other on the left and right. The circuit configuration of each of the second signal processing units 21L and 21R is the same as that of each of the second signal processing units 14L and 14R described in the first embodiment, and each of them is constituted by, e.g., FIR type digital filters. In this configuration, however, delay amounts set in the signal processing units 21L and 21R are variably set depending on a rotational angle arithmetically processed by a rotational angle arithmetic processing unit 24.

The left and right signals subjected to the reflective sound adding processes by the signal processing units 21L and 21R are respectively supplied to different digital/analog converters 15L and 15R for the respective channels to be converted into analog audio signals. The left and right two-channel analog audio signals are amplified by amplifiers 16L and 16R, having relatively small amplification factors for driving a

headphone, and the amplified audio signals are then supplied to headphone connection terminals 17L and 17R. The audio signals of the respective channels obtained from the headphone connection terminals 17L and 17R are supplied to left and right loudspeaker units 22L and 22R of a headphone device 22 connected to the headphone connection terminals 17L and 17R, respectively, and the audio signals are reproduced from the headphone device 22.

In this case, the headphone device 22 according to this embodiment has a configuration including a rotational angular velocity sensor 23 such that a rotational angular velocity parallel to the head of a listener who wears the headphone device 22 is detected. As the rotational angular velocity sensor 23, e.g., a piezoelectric vibration gyro is used. A detection output from the rotational angular velocity sensor 23 is supplied to the rotational angle arithmetic processing unit 24 on the processing apparatus side. The rotational angle arithmetic processing unit 24 is constituted by a microprocessor for arithmetically operating an rotational angle of the headphone device 22 on the basis of the detection output from the rotational angular velocity sensor 23. For example, an output from the rotational angular velocity sensor 23 is subjected to sampling at a constant time interval and then integrated, and the integration result is converted into angle data.



On the basis of the obtained angle data, the process of correcting delay amounts and a level difference used in the processes performed in the second signal processing units 21L and 21R is carried out and a process in which a sound image is localized in a predetermined direction outside the head of the listener wearing the headphone device 22 is performed.

As the process of correcting delay amounts and the level difference set in the respective signal processing units 21L and 21R depending on the detected rotational angle, the following process is performed. That is, depending on the rotational angle of the head of a listener, the multiplication coefficients of the digital filters are updated on real time by control of the rotational angle arithmetic processing unit 24 such that transfer functions corresponding to the rotational angle are realized. In this process, if it is considered that the listener turns her/his head to the right, the sound reaching the left ear becomes earlier than the sound reaching the right ear. In addition, the left ear becomes close to the sound source, while the right ear becomes distant from the sound source. For this reason, the level of the signal reaching the left ear becomes higher than the level of the signal reaching the right ear. When this phenomenon is represented by transfer functions which represents the phenomenon in a pseudo manner, changes in delay time are as shown in FIG. 8, for example. A characteristic A shown in FIG. 8 indicates a change in delay time added to the

right-channel signal depending on an angle, and a characteristic B shown in FIG. 8 indicates a change in delay time added to the left-channel signal depending on an angle. The characteristics A and B are change characteristics of broken lines. In the characteristics obtained by changes in angle, a change in level of the left-channel signal is given by a change indicated by a curve C in FIG. 9, and a change in level of the right-channel signal is given by a change indicated by a curve D in FIG. 9, for example. When the delay amounts and the levels set by the signal processing units 21L and 21R are set depending on the angles as shown in FIGS. 8 and 9, correction depending on the rotational angle of the head of the listener can be performed.

With the configuration described above, similarly as in the first embodiment, a sound field reproduced by the headphone device 22 and heard by the listener is a preferable sound field which is similar to a sound field formed such that original two-channel audio signals are reproduced by loudspeakers arranged in a room or the like. Since the process is performed by the first signal processing unit 13 and the second signal processing units 21L and 21R, similarly as in the first embodiment, the apparatus can be realized by a simple circuit configuration having a small quantity of arithmetic processing. In this embodiment, the correction process in which the sound image is localized in a predetermined direction outside the head of the listener with the headphone device is performed simultaneously with the

processes in the second signal processing units 21L and 21R. For this reason, as circuits required for the process of correcting the localization direction of the sound image, only the angular velocity attached to the headphone device and the arithmetic operation means for obtaining angle data from the output from the angular velocity sensor may be sufficient. The process of correcting the localization direction of the sound image can be performed by the simple circuit configuration.

By the way, as the means for detecting the direction in which the headphone device 22 faces, the angular velocity sensor is used. However, a configuration in which a geomagnetic sensor for detecting an absolute azimuth is used to cause an output from the geomagnetic sensor to detect the direction may be used.

A third embodiment of the present invention will be described below with reference to FIG. 10. The same reference numerals as those in FIGS. 1 to 6 explained in the first embodiment described above denote the same parts in FIG. 10, and a description thereof will be omitted.

In this embodiment, multi-channel audio signals obtained at input terminals 31L, 31R, 31C, 31SL, 31SR, and 31LFE are converted into two-channel audio signals for the binaural reproduction, and the two-channel audio signals are supplied to a headphone device connected to the apparatus to reproduce the two-channel audio signals.

The configuration of this embodiment will be described

blow. FIG. 10 is a block diagram showing the entire configuration of this embodiment. Multi-channel audio signals supplied to the input terminals of this embodiment are constituted by six-channel audio signals. That is, a left-front-channel signals is obtained at the input terminal 31L, a right-front-channel signal is obtained at the input terminal 31R, and a center-channel signal is obtained at the input terminal 31C. A left-rear-channel signal is obtained at the input terminal 31SL, a right-rear-channel signal is obtained at the input terminal 31SR and a signal of a low-band-only channel is obtained at the input terminal 31LFE. In this channel configuration, the low-band-only channel is considered as a 0.1 channel, and the 0.1 channel and the five remaining channels may be called 5.1 channels in some case. The low-band-only channel is a channel from which only an audio signal in a band lower than, e.g., about 120 Hz can be obtained.

The audio signals obtained at the respective input terminals 31L, 31R, 31C, 31SL, 31SR, and 31LFE are respectively supplied to different analog/digital converters 32L, 32R, 32C, 32SL, 32SR, and 32LFE for the respective channels to be converted into analog audio signals, independently. The converted audio signals of the respective channels are supplied to a distribution processing unit 33. In the distribution processing unit 33, the process of equally mixing the center-channel signal with the signals of left and right front channels

is performed, and at the same time the process of equally mixing the signal of the low-band-only channel with the signals of the other channels is performed, so that four-channel signals, i.e., left and right front audio signals SLa and SRa and left and right rear audio signals SLb and SRb are obtained.

The four-channel audio signals are supplied to a digital processing unit 34 to perform the process of converting the two-channel audio signals into audio signals SLc and SRC of left and right two channels having sound sources located at four different positions surrounding a listener. This conversion process is performed by using, e.g., a digital filter, an adder and a subtractor.

The left and right two-channel audio signals SLc and SRC converted by the digital processing unit 34 are supplied to a first signal processing unit 13. The first signal processing unit 13 is a circuit for performing the process of converting audio signals into two-channel audio signals for forming a sound field for the headphone reproduction on the basis of two series of impulse responses from sound sources to the left and right ears of the listener. This circuit is entirely the same as the circuit which has been described in connection with the first embodiment.

The left-channel audio signal processed by the first signal processing unit 13 is supplied to a second signal processing unit 14L for the left channel, and the right-channel

audio signal processed by the first signal processing unit 13 is supplied to a second signal processing unit 14R for the right channel. In the second signal processing units 14L and 14R, reflective sound adding processes are independently performed by transfer functions which are not correlated to each other on the left and right. The circuit configuration of the second signal processing units 14L and 14R is the same as that of the second signal processing units 14L and 14R described in connection with the first embodiment.

The left and right signals subjected to the reflective sound adding processes by the signal processing units 14L and 14R are respectively supplied to different digital/analog converters 15L and 15R for the respective channels to be converted into analog audio signals. The left and right two-channel analog audio signals are amplified by amplifiers 16L and 16R, having relatively small amplification factors, for driving a headphone, and the amplified audio signals are supplied to headphone connection terminals 17L and 17R. The audio signals of the respective channels obtained from the headphone connection terminals 17L and 17R are supplied to left and right loudspeaker units 18L and 18R of a headphone device 18 connected to the headphone connection terminals 17L and 17R, respectively, and the audio signals are reproduced from the headphone device 18.

With the configuration described above, a sound field

having sound sources located positions surrounding the listener wearing the headphone device 18 is formed the by multi-channel audio signals, and hence the multi-channel audio signals can be preferably reproduced. In this case, similarly as in the first embodiment, since the first signal processing unit 13 and the second signal processing units 14L and 14R are separately arranged, the process of converting signals into signals of a sound field reproduced by a headphone device can be performed by a simple circuit configuration.

This embodiment has explained the process performed when 5.1-channel audio signals are input as multi-channel audio signals. However, the embodiment can also be applied to multi-channel audio signals having another channel configuration as a matter of course.

In addition, when the process of reproducing the multi-channel audio signals is to be performed, it may be possible that the correction process depending on the rotational angle of a head described in the second embodiment is performed, and a position where a sound image is localized always faces in a constant direction even if the head is turned.

In each of the embodiments described up to now, the apparatus for processing supplied audio signals and the headphone device are directly connected to each other with a signal line. However, for example, as a configuration in which audio signals obtained from the output terminals 17L and 18R of

the apparatus shown in FIG. 1, FIG. 7, or FIG. 10 are transmitted in wireless to the headphone device through infrared signals or the like, a so-called wireless headphone device in which signals transmitted in wireless are received by a headphone device may be used. In this case, the angular velocity data explained in connection with the second embodiment may be transmitted in wireless to the processing apparatus.

Having described preferred embodiments of the invention with reference to the accompanying drawings, it is to be understood that the invention is not limited to those precise embodiments and that various changes and modifications could be effected therein by one skilled in the art without departing from the spirit or scope of the invention as defined in the appended claims.



What is claimed is:

1. An audio processing apparatus comprising:

a first filter means for converting n-channel ( $n \geq 1$ , positive integer) audio signals supplied from at least one signal source into two-channel signals;

a pair of second filter means to which two-channel output signals from the first filter means are supplied and which provides an uncorrelated processing means for setting different delay times for transfer functions to the supplied two-channel input signals; and

an output unit for supplying output signals output from the pair of second filter means to left and right loudspeaker units of a headphone.

2. An audio processing apparatus according to claim 1, wherein the pair of second filter means are constituted by a digital filter, and a pair of uncorrelated processing means for setting delay times for transfer functions are constituted by delay units having different delay times.

3. An audio processing apparatus according to claim 1, wherein the pair of second filter means are constituted by a digital filter, and a pair of uncorrelated processing means for setting delay times for transfer functions are constituted by a

delay unit for outputting a plurality of delay times, a multiplier for setting each delay time output to an arbitrary value, and an adder for adding each multiplier output.

4. An audio processing apparatus according to claim 1, wherein the pair of first filter means are constituted by digital filters having characteristics same or equivalent in transfer characteristics.

5. An audio processing apparatus according to claim 1, further comprising a detection means for detecting a direction of movement of the head of a listener wearing the headphone, wherein the transfer functions of the pair of second filter means are made variable depending on an output from the detection means.

6. An audio processing apparatus according to claim 1, wherein the detection means for detecting a direction of movement of the head of a listener wearing the headphone is a piezoelectric vibration gyro, and the transfer functions of the pair of second filter means are made variable depending on an output from the piezoelectric vibration gyro.

7. An audio processing apparatus according to claim 1, wherein the detection means for detecting a direction of

movement of the head of a listener wearing the headphone is a geomagnetic azimuth sensor, and the transfer functions of the pair of second filter means are made variable depending on an output from the geomagnetic azimuth sensor.

8. An audio reproducing method comprising:

a first conversion process of converting n-channel ( $n \geq 1$ , positive integer) audio signals supplied from at least one signal source into two-channel signals on the basis of two series of impulse responses from a sound source to left and right ears of a listener;

a second conversion process of independently performing reflective sound adding processes for a pair of uncorrelated processing means for setting a delay time to transfer functions with respect to two-channel output signals from the first conversion processing means, and

a process of reproducing two-channel output signals subjected to the second conversion process near left and right ears of the listener.

# ABSTRACT OF THE DISCLOSURE

An audio processing apparatus is disclosed which includes a first filter means for converting n-channel ( $n \geq 1$ , positive integer) audio signals input from at least one signal source into two-channel signals, a pair of second filter means to which two-channel output signals from the first filter means are input and which have transfer functions with an uncorrelation, and an output unit for supplying a pair of output signals from the pair of second filter means to left and right loudspeaker units of a headphone device.

FIG. 1

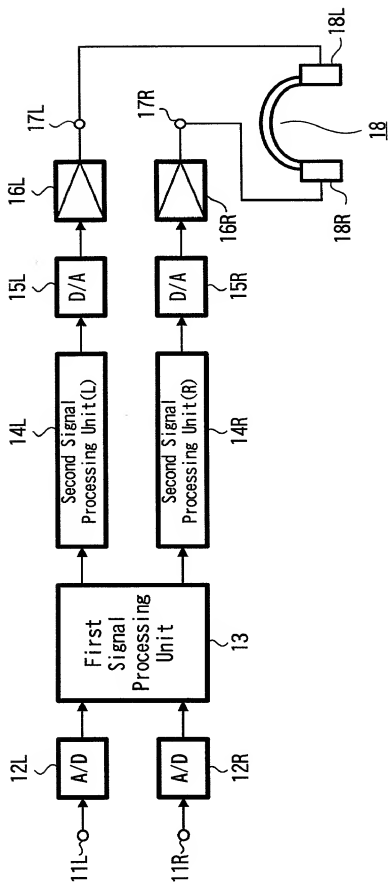


FIG. 2

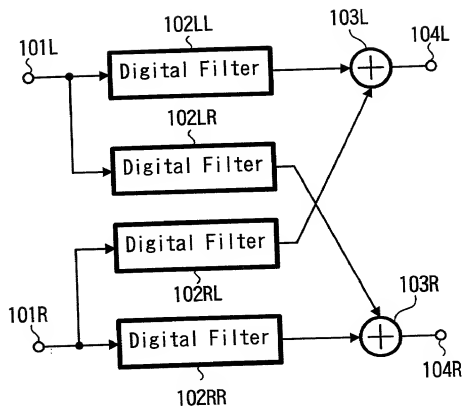


FIG. 3

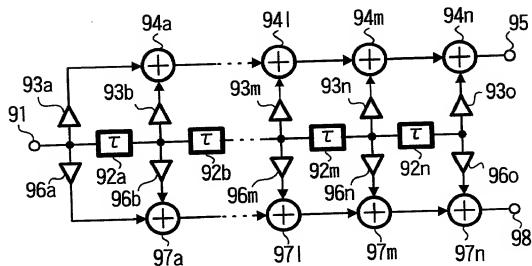


FIG. 4

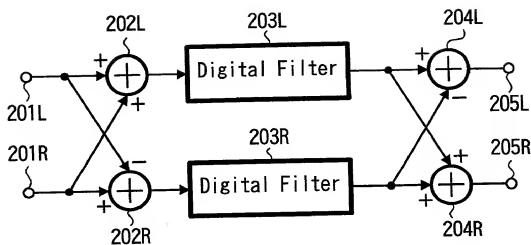
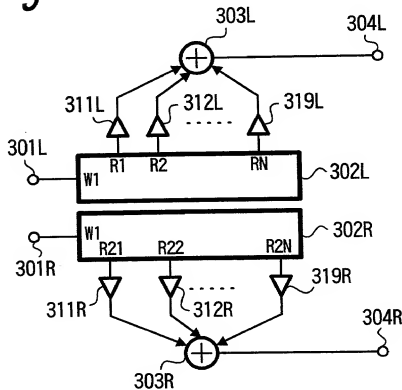
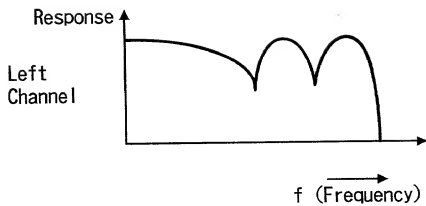


FIG. 5



**FIG. 6A**



**FIG. 6B**

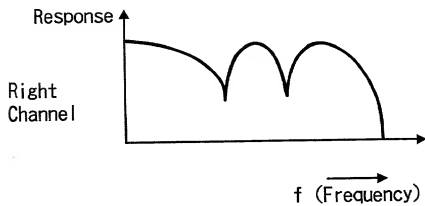
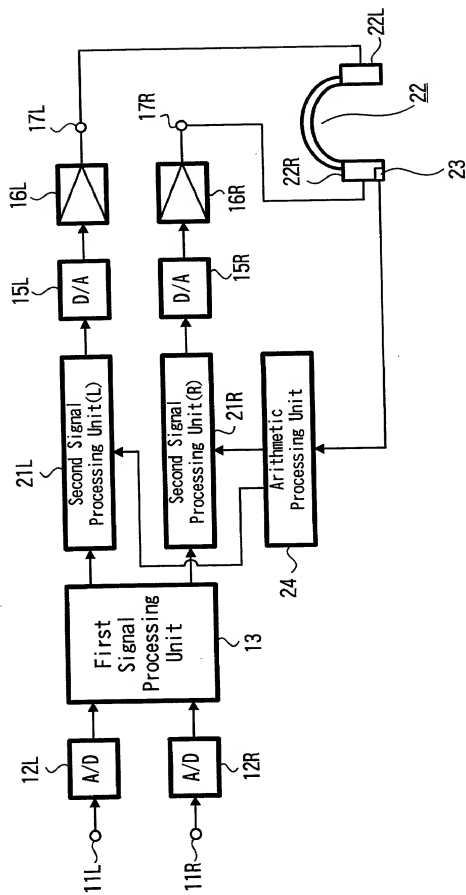
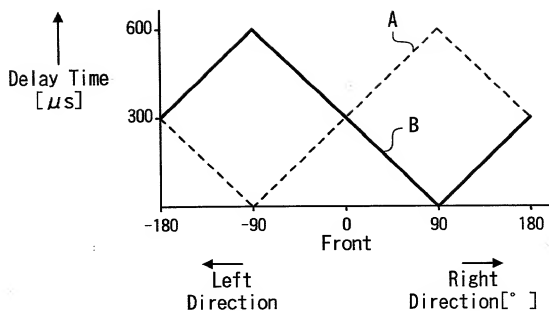




FIG. 7



**FIG. 8**



**FIG. 9**

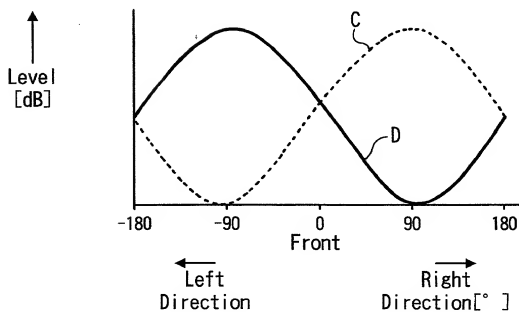


FIG. 10

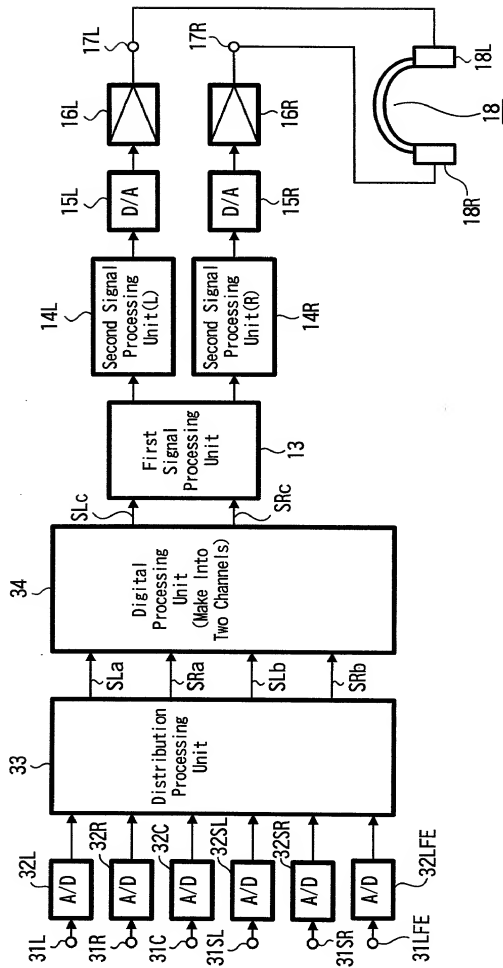


FIG. 11 (PRIOR ART)

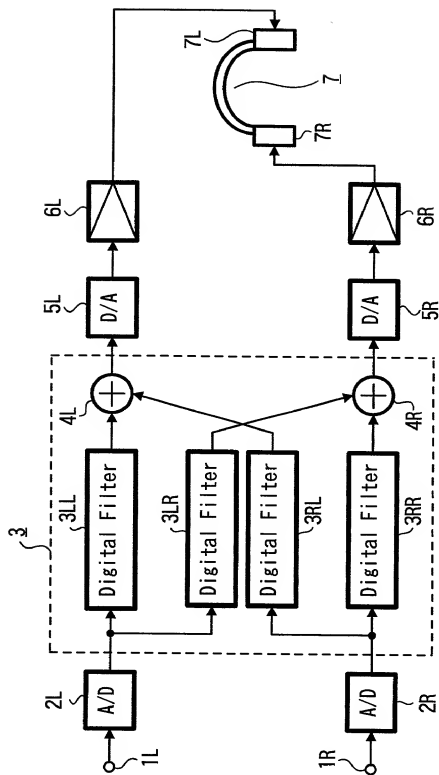


FIG. 12

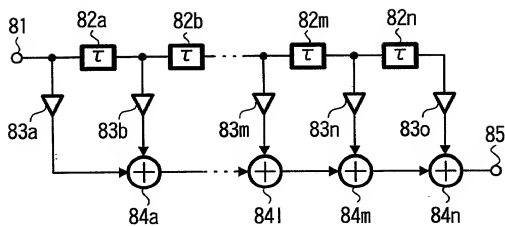
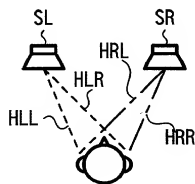


FIG. 13



DECLARATION AND POWER OF ATTORNEY

As a below-named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

AUDIO PROCESSING APPARATUS AND AUDIO REPRODUCING METHOD

The specification of which  
(check one)

  X   is attached hereto.

       was filed on                                  as                                 

Application Serial No.   

and was amended on     
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which I am aware which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

<u>Number</u>	<u>Country</u>	<u>Filing Date</u>	<u>Yes</u>	<u>No</u>
<u>P10-311308</u>	<u>Japan</u>	<u>October 30, 1998</u>	<u>  X  </u>	<u>      </u>

Declaration and Power of Attorney

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I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States Application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

<u>Application Serial No.</u>	<u>Filing Date</u>	<u>Status</u>
_____	_____	_____
_____	_____	_____

And I hereby appoint Jay H. Maioli, Reg. No. 27,213; Donald S. Dowden, Reg. No. 20,701; William E. Pelton, Reg. No. 25,702; Peter J. Phillips, Reg. No. 29,691; Gerald W. Griffin, Reg. No. 18,886; Ivan S. Kavrukov, Reg. No. 25,161; Christopher C. Dunham, Reg. No. 22,031; Norman H. Zivin, Reg. No. 25,385; John P. White, Reg. No. 28,678; and Robert D. Katz, Reg. No. 30,141; and each and all of them, all c/o Cooper & Dunham, 1185 Avenue of the Americas, New York, NY 10036 (Tel. (212) 278-0400), my attorneys, each with full power of substitution and revocation, to receive the patent, to transact all business in the Patent and Trademark Office connected therewith and to file any International Applications which are based thereon under the provisions of the Patent Cooperation Treaty.

Please address all communications, and direct all telephone calls, regarding this application to

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Cooper & Dunham LLP  
1185 Avenue of the Americas  
New York, New York 10036  
Tel. (212) 278-0400

Reg. No. 27,213

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or

First joint inventor Yuji Yamada

Inventor's signature \_\_\_\_\_

Citizenship Japanese Date of Signature \_\_\_\_\_

Residence Tokyo, Japan

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